

[MICROPROCESSOR SYSTEM ARCHITECTURE TO CORRECT BUILT-IN ROM CODE]

Abstract of Disclosure

A microprocessor system to correct built-in ROM code includes a program counter and an address space divided into a program ROM (read only memory) space, a small address RAM that is a subset of the program ROM, a working RAM (random access memory), and a small program RAM. When the program counter accesses instructions in the program ROM, the lower bits of the program counter are simultaneously accessing an entry in the address RAM. When a valid bit of the accessed entry indicates and the page number of the accessed entry is the same as the page number in the program counter, the jump address in the accessed entry is placed into the program counter so that corrected code in the program RAM will be executed instead of the indicated faulty code in the program ROM.

Figures